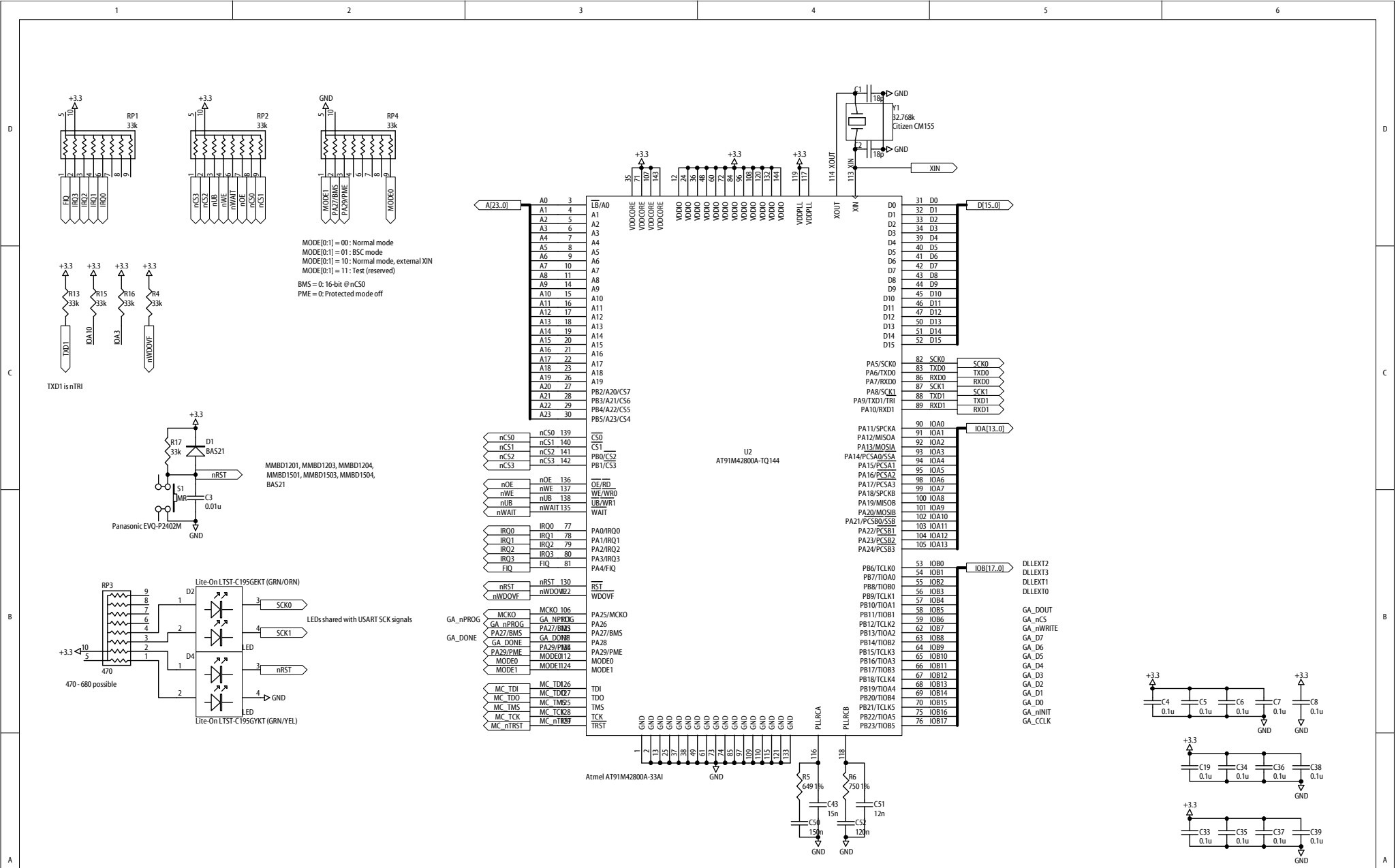


Title		
ARMermelator		
Size	Number	Revision
B	1	0
Date:	29-Dec-2004	Sheet 1 of 6
File:	D:\Design\ARMermelator\ARMermelator-v1.2.0	

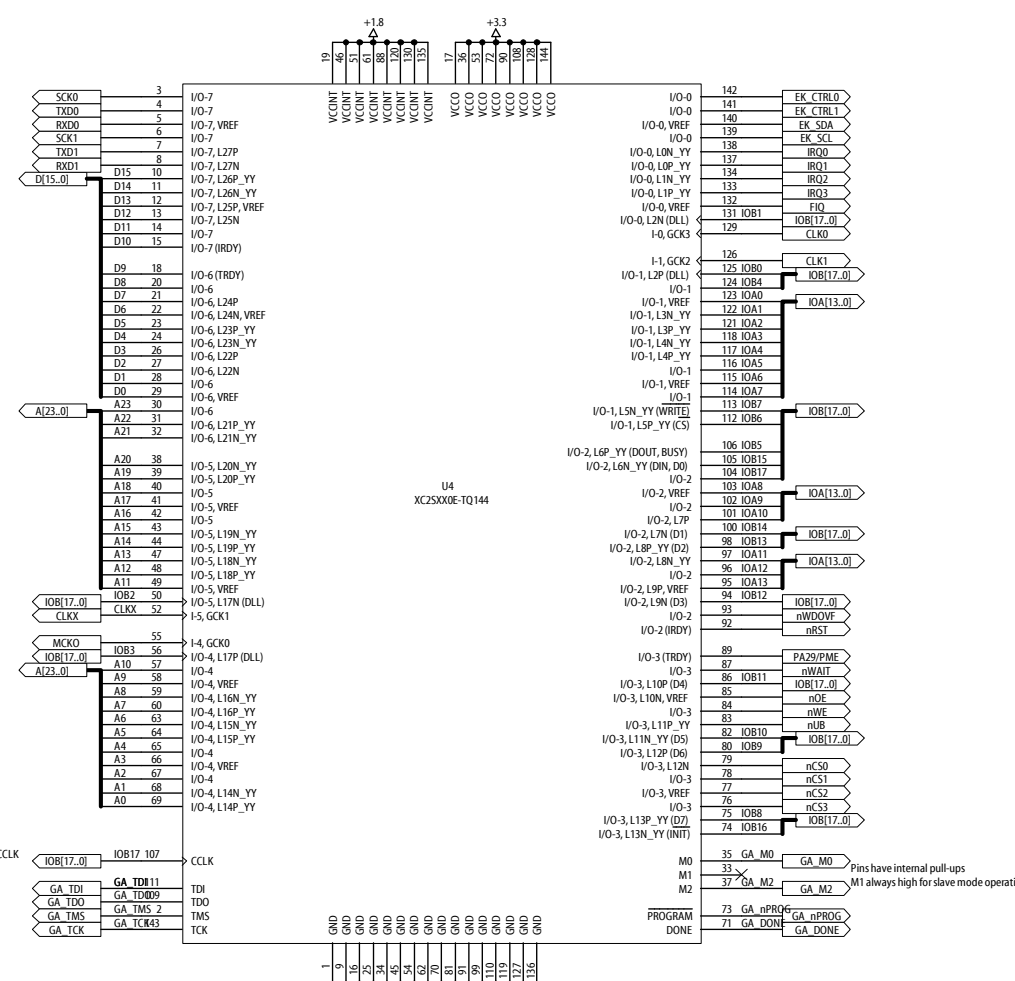


MODE[0:1] = 00: Normal mode
 MODE[0:1] = 01: BSC mode
 MODE[0:1] = 10: Normal mode, external XIN
 MODE[0:1] = 11: Test (reserved)
 BMS = 0: 16-bit @ nCS0
 PME = 0: Protected mode off

MMBD1201, MMBD1203, MMBD1204,
 MMBD1501, MMBD1503, MMBD1504,
 BAS21

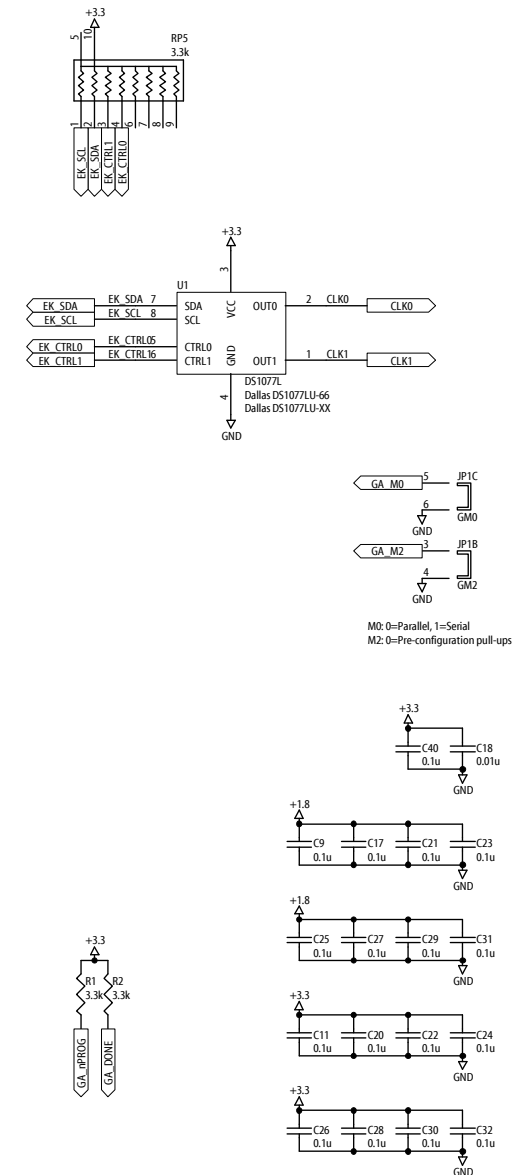
LEDs shared with USART SCK signals

Title		Control	
Size	Number	Revision	
B	1	0	
Date:	29 Dec-2004		Sheet 2 of 6
File:	D:\Design\ARM\melator\ARM\melator-v1.2.0\Drawn By:		

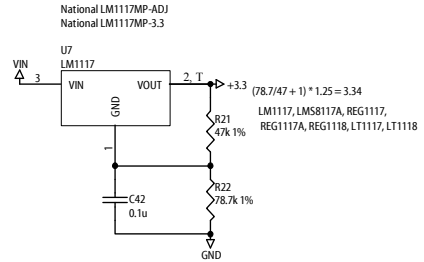
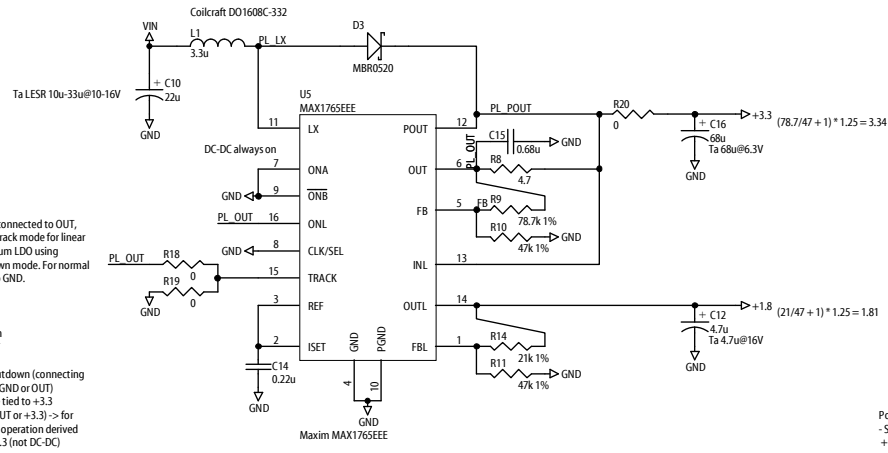


Xilinx XC2550E-6TQ144C
Xilinx XC25100E-6TQ144C

v1.2: Missing pull-up for GA_nNINT
-WD: Add 100ps delay after deasserting GA_nPROG



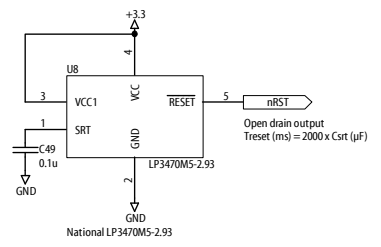
Title		Logic	
Size	Number	Revision	
B	1	0	
Date:	29 Dec-2004	Sheet 3of 6	
File:	D:\Design\ARM\emulator\ARM\emulator-v1.2.0	Drawn By:	



-When TRACK is connected to OUT, device enters in track mode for linear regulator minimum LDO using step-up/step-down mode. For normal mode connect to GND.

v1.2:
 -DC-DC always on
 -ONL tied to OUT
 TBD:
 -Allow DC-DC shutdown (connecting ONA or NONB to GND or OUT)
 -Allow ONL to be tied to +3.3 (connecting to OUT or +3.3) -> for stand-alone LDO operation derived from external +3.3 (not DC-DC)

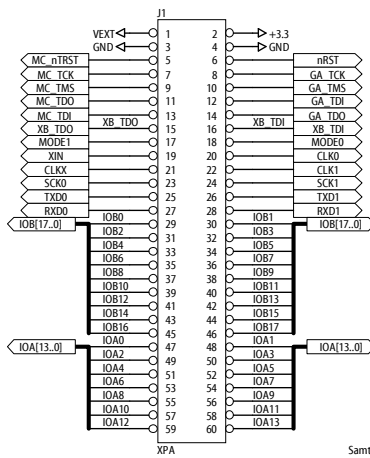
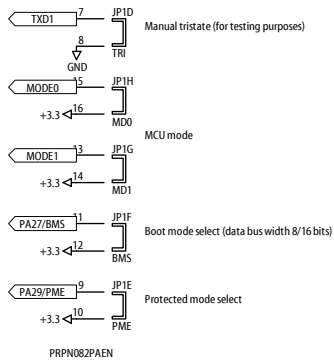
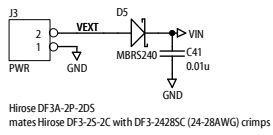
MODE	TRACK	ONA	ONB	ONL	LDO	DCDC	REF
Shutdown	X	L	H	L	OFF	OFF	OFF
Track	H	H	X	X	ON	ON	ON
Independent Regulation	L	H	X	H	ON	ON	ON
DC-DC Only	L	X	L	L	OFF	OFF	ON
LDO Only	X	L	H	H	ON	OFF	ON



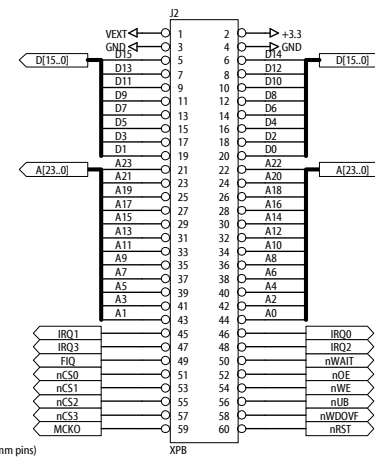
Power configurations:
 - Step-up +1.8, +3.3:
 + Use MAX1765 in normal configuration, +1 - +3.5 VDC input, 800mA-max @ +3.3 plus 500mA-max @ +1.8
 + If +1.8 not needed, shutdown LDO
 - Step-up +1.8, Regulator for +3.3 (independent supplies):
 + Use MAX1765 in track mode for +1.8 (POUT disconnected from +3.3), LM1117 for +3.3 (+5.5 max VIN)
 - Regulators for +1.8, +3.3 (chained supplies):
 + Use LM1117 for +3.3, MAX1765 with DCDC shutdown and LDO enabled, +1.8 derived from +3.3 (at INL)
 - Regulator for +3.3, no +1.8:
 + Use only LM1117 for +3.3



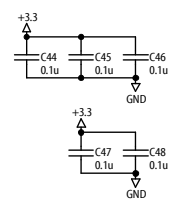
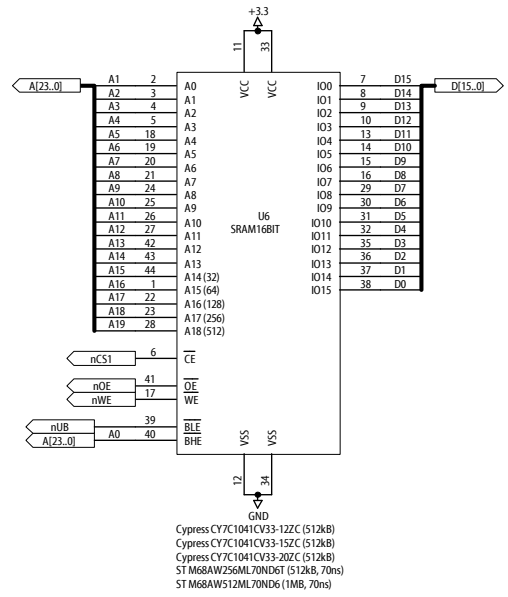
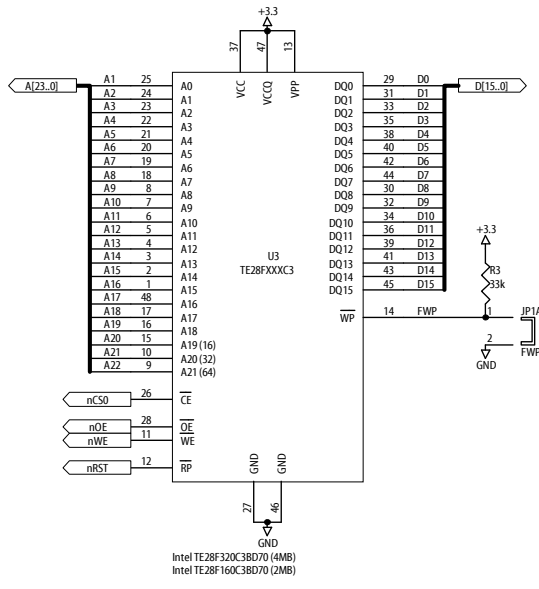
Title		
Power		
Size	Number	Revision
B	1	0
Date:	29 Dec-2004	Sheet Sof 6
File:	D:\Design\ARM\melator\ARM\melator\v1.2.0\Drawn By:	



Samtec SQT-130-03-F-D (5mm pins)
Sullins PPWN302AFHN (5mm pins)
3M 150260-6002TB (2.54mm pins)



Title		
Connector		
Size	Number	Revision
B	1	0
Date:	29 Dec-2004	Sheet 6of 6
File:	D:\Design\ARMermelator\ARMermelator-v1.2.0\Drawn By:	



Title		
Memory		
Size	Number	Revision
B	1	0
Date:	29-Dec-2004	
File:	D:\Design\ARM\marmelator\ARM\marmelator-v1.2.0\Drawn By:	
		Sheet 4of 6